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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kevin Frazier, et al.  
Serial No: Herewith  
Filed: Herewith  
For: MULTI-PROTOCOL PACKET TRANSLATOR

Examiner: Not Assigned  
Art Unit: Not Assigned

BOX PATENT APPLICATION  
COMMISSIONER FOR PATENTS  
WASHINGTON, D.C. 20231

Sir:

PRELIMINARY AMENDMENT

In the Specification

Page 5, line 12, after "While" insert --the--

Page 7, line 12, after "(MPT)" insert --26--

Page 11, line 8, after "processing." insert Addressing of the dump memory 57 may be  
controlled by an address control unit 57a.--

Page 16, line 2, after "FIG. 5" insert --STEP 85--

Page 16, line 7, after "translation" insert --88--

Page 25, line 18, after "stage" insert --112--

Page 25, line 18, after "instruction" insert --112a-112d--

Page 26, line 11, after "stage" insert --112--

Page 28, line 18, after "written" insert --155--

In the Claims

Please add the following new claims 22-33:

22. A pipelined control unit for controlling pipelined translations between a first data packet having a first protocol and a second data packet having a second protocol, comprising:

491188.1

#4/A  
L. McLaughlin  
8.7.01

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